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		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L12	l1 and snmp	0
<input type="checkbox"/>	L11	L10 and l1	1
<input type="checkbox"/>	L10	(pre\$post) near5 (buffer\$1)	14
<input type="checkbox"/>	L9	l1 and (switched near5 fabric)	4
<input type="checkbox"/>	L8	l1 and datagram	4
<input type="checkbox"/>	L7	l1 and subnet	3
<input type="checkbox"/>	L6	L5 and traffic	3
<input type="checkbox"/>	L5	L4 and node\$1	6
<input type="checkbox"/>	L4	L3 and host	10
<input type="checkbox"/>	L3	l1 and messages	28
<input type="checkbox"/>	L2	l1 and (queue near5 pair\$1)	9
<input type="checkbox"/>	L1	(buffer\$1 and queue\$).ti.	392

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Search Results - Record(s) 1 through 9 of 9 returned.

☐ 1. Document ID: US 20030061417 A1

L2: Entry 1 of 9

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061417

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030061417 A1

TITLE: Infiniband work and completion queue management via head and tail circular buffers with indirect work queue entries

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Craddock, David F.	New Paltz	NY	US	
Gregg, Thomas Anthony	Highland	NY	US	
Judd, Ian David	Winchester Hampshire	TX	GB	
Pfister, Gregory Francis	Austin	TX	US	
Recio, Renato John	Austin	NY	US	
Schmidt, Donald William	Stone Ridge		US	

US-CL-CURRENT: 710/54

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 2. Document ID: US 20030058875 A1

L2: Entry 2 of 9

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030058875

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030058875 A1

TITLE: Infiniband work and completion queue management via head only circular buffers

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Arndt, Richard Louis	Austin	TX	US	

Craddock, David F.	New Paltz	NY	US
Gregg, Thomas Anthony	Highland	NY	US
Judd, Ian David	Otterbourne	TX	GB
Pfister, Gregory Francis	Austin	TX	US
Recio, Renato John	Austin	NY	US
Schmidt, Donald William	Stone Ridge		US

US-CL-CURRENT: 370/412; 370/328

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 3. Document ID: US 6789143 B2

L2: Entry 3 of 9

File: USPT

Sep 7, 2004

US-PAT-NO: 6789143

DOCUMENT-IDENTIFIER: US 6789143 B2

TITLE: Infiniband work and completion queue management via head and tail circular buffers with indirect work queue entries

DATE-ISSUED: September 7, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Craddock; David F.	New Paltz	NY		
Gregg; Thomas Anthony	Highland	NY		
Judd; Ian David	Winchester			GB
Pfister; Gregory Francis	Austin	TX		
Recio; Renato John	Austin	TX		
Schmidt; Donald William	Stone Ridge	NY		

US-CL-CURRENT: 710/54; 710/52, 710/57, 711/147, 711/153, 711/173

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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☐ 4. Document ID: JP 2003216592 A

L2: Entry 4 of 9

File: JPAB

Jul 31, 2003

PUB-NO: JP02003216592A

DOCUMENT-IDENTIFIER: JP 2003216592 A

TITLE: METHOD AND DEVICE FOR MANAGING INFINIBAND WORK AND COMPLETION QUEUE VIA HEAD ONLY CIRCULAR BUFFER

PUBN-DATE: July 31, 2003

INVENTOR-INFORMATION:

NAME	COUNTRY
------	---------

ARNDT, RICHARD LOUIS
CRADDOCK, DAVID F
GREGG, THOMAS A
JUDD, IAN DAVID
PFISTER, GREGORY FRANCIS
RECIO, RENATO JOHN
DONALD, WILLIAM SCHMIDT

INT-CL (IPC): G06 F 15/17

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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☐ 5. Document ID: US 20040193811 A1

L2: Entry 5 of 9

File: DWPI

Sep 30, 2004

DERWENT-ACC-NO: 2004-708953

DERWENT-WEEK: 200469

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TITLE: Queuing mechanism for client-server system, has multiple queue pairs, each having receive queue associated with buffers of shared receive queue

INVENTOR: CHADALAPAKA, M; CULLEY, P R ; GARCIA, D J ; HILLAND, J R

PRIORITY-DATA: 2003US-0401231 (March 27, 2003)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 20040193811 A1	September 30, 2004		010	G06F012/00

INT-CL (IPC): G06 F 12/00

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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☐ 6. Document ID: US 20030101158 A1

L2: Entry 6 of 9

File: DWPI

May 29, 2003

DERWENT-ACC-NO: 2003-635378

DERWENT-WEEK: 200360

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TITLE: Data message managing method in local area network, involves posting client specified buffers at management queue pairs to receive incoming data message, if prepost buffers are specified for client

INVENTOR: PINTO, O P; SHAH, R R

PRIORITY-DATA: 2001US-0994779 (November 28, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 20030101158 A1	May 29, 2003		018	G06F007/00

INT-CL (IPC): G06 F 7/00

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWAC	Draw D.
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☐ 7. Document ID: US 6466993 B1

L2: Entry 7 of 9

File: DWPI

Oct 15, 2002

DERWENT-ACC-NO: 2003-045694

DERWENT-WEEK: 200304

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TITLE: Computer system has inbound and outbound queue structures with free-list and post-list buffers that are locally connected to host processor

INVENTOR: BONOLA, T J

PRIORITY-DATA: 1998US-0186540 (November 5, 1998)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 6466993 B1	October 15, 2002		020	G06F003/00

INT-CL (IPC): G06 F 3/00

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWAC	Draw D.
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☐ 8. Document ID: US 6377544 B1

L2: Entry 8 of 9

File: DWPI

Apr 23, 2002

DERWENT-ACC-NO: 2002-442665

DERWENT-WEEK: 200247

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TITLE: Data directing method e.g. for audio data, video data, involves computing amount of data to be routed across each link, as a function of difference between amount of data in each queue buffer of switch

INVENTOR: MUTHUKRISHNAN, S; SUEL, T

PRIORITY-DATA: 1998US-0136819 (August 20, 1998)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 6377544 B1	April 23, 2002		014	H04L012/26

INT-CL (IPC): H04 L 12/26; H04 L 12/56

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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☐ 9. Document ID: US 6195335 B1

L2: Entry 9 of 9

File: DWPI

Feb 27, 2001

DERWENT-ACC-NO: 2001-326748

DERWENT-WEEK: 200134

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TITLE: Packet data switch in packet switched data communication systems, inhibits selection of queues corresponding to input-output pairs for which respective cross point buffer is full

INVENTOR: BASSO, C; CALVIGNAC, J ; ORSATTI, D ; TOUBOL, G ; VERPLANKEN, F

PRIORITY-DATA: 1997EP-0480041 (June 27, 1997)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 6195335 B1	February 27, 2001		008	H04L012/56

INT-CL (IPC): H04 L 12/56

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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Term	Documents
QUEUE	63310
QUEUES	26526
PAIR\$1	0
PAIR	2908966
PAIRA	95
PAIRB	19
PAIRC	34
PAIRD	51
PAIRE	241
PAIRF	28
(L1 AND (QUEUE NEAR5 PAIR\$1)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	9

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L9: Entry 1 of 4

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061417

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030061417 A1

TITLE: Infiniband work and completion queue management via head and tail circular buffers with indirect work queue entries

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Craddock, David F.	New Paltz	NY	US	
Gregg, Thomas Anthony	Highland	NY	US	
Judd, Ian David	Winchester Hampshire	TX	GB	
Pfister, Gregory Francis	Austin	TX	US	
Recio, Renato John	Austin	NY	US	
Schmidt, Donald William	Stone Ridge		US	

US-CL-CURRENT: 710/54

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KLOC	Draw D
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☐ 2. Document ID: US 20030058875 A1

L9: Entry 2 of 4

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030058875

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030058875 A1

TITLE: Infiniband work and completion queue management via head only circular buffers

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Arndt, Richard Louis	Austin	TX	US	

Craddock, David F.	New Paltz	NY	US
Gregg, Thomas Anthony	Highland	NY	US
Judd, Ian David	Otterbourne	TX	GB
Pfister, Gregory Francis	Austin	TX	US
Recio, Renato John	Austin	NY	US
Schmidt, Donald William	Stone Ridge		US

US-CL-CURRENT: 370/412; 370/328

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 3. Document ID: US 6789143 B2

L9: Entry 3 of 4

File: USPT

Sep 7, 2004

US-PAT-NO: 6789143

DOCUMENT-IDENTIFIER: US 6789143 B2

TITLE: Infiniband work and completion queue management via head and tail circular buffers with indirect work queue entries

DATE-ISSUED: September 7, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Craddock; David F.	New Paltz	NY		
Gregg; Thomas Anthony	Highland	NY		
Judd; Ian David	Winchester			GB
Pfister; Gregory Francis	Austin	TX		
Recio; Renato John	Austin	TX		
Schmidt; Donald William	Stone Ridge	NY		

US-CL-CURRENT: 710/54; 710/52, 710/57, 711/147, 711/153, 711/173

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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☐ 4. Document ID: US 6259698 B1

L9: Entry 4 of 4

File: USPT

Jul 10, 2001

US-PAT-NO: 6259698

DOCUMENT-IDENTIFIER: US 6259698 B1

TITLE: Input buffer controller using back-pressure signals in ATM switches and a method for determining the logical queue size

DATE-ISSUED: July 10, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Shin; Jae-Jin	Kwangmyong			KR
Lee; Kyung-Geun	Sungnam			KR
Sung; Dan-Keun	Daejon			KR
Heo; Jeong-Won	Daejon			KR
Byun; Sung-Hyuk	Siheung			KR
Lee; Ju-Yong	Daegoo			KR
Yang; Jin-Woo	Busan			KR

US-CL-CURRENT: 370/395.7; 370/235

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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Fwd Refs

Bkwd Refs

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Term	Documents
SWITCHED	580629
SWITCHEDS	8
FABRIC	507588
FABRICS	161787
(1 AND (SWITCHED NEAR5 FABRIC)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4
(L1 AND (SWITCHED NEAR5 FABRIC)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4

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☐ 1. Document ID: US 20030101158 A1

L11: Entry 1 of 1

File: DWPI

May 29, 2003

DERWENT-ACC-NO: 2003-635378

DERWENT-WEEK: 200360

COPYRIGHT 2005 DERWENT INFORMATION LTD

TITLE: Data message managing method in local area network, involves posting client specified buffers at management queue pairs to receive incoming data message, if prepost buffers are specified for client

INVENTOR: PINTO, O P; SHAH, R R

PRIORITY-DATA: 2001US-0994779 (November 28, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 20030101158 A1	May 29, 2003		018	G06F007/00

INT-CL (IPC): G06 F 7/00

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
(10 AND 1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1
(L10 AND L1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1

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[Google \(Web\)](#) [Yahoo!](#) [MSN](#) [CSB](#) [DBLP](#)

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[Integration of Constraint Reasoning, and Simulation models in.. - Adhikary](#) (Correct)

problem is an important part of forest resource management. It is a complex task requiring expertise and called thrashing whereby the same variable-value pair that leads to no solution is instantiated over and over. fas.sfu.ca/isl/papers/adhikary-integrating.ps.gz

[IT Security and Smart Card Standards - Vesna Hassler](#) (Correct)

eighteen elected member bodies. The Technical Management Board has twelve (1995) members appointed by www.infosys.tuwien.ac.at/Staff/vh/papers/std.ps.gz

[Predictive Workflow Management - Panagos, Rabinovich \(1997\)](#) (Correct)

Predictive Workflow Management Euthimios Panagos and Michael Rabinovich AT&T
 it includes any queuing time at the agent's work queue -such estimates can be easily extracted from www.research.att.com/~thimios/papers/ngits97.ps.Z

[Satellite Atm Network Architectural Considerations And.. - Kota, Goyal, Jain \(1997\)](#) (Correct) (3 citations)

drop policies to improve utilization, per-VC buffer management to improve fairness, and even minimum policies to improve utilization, per-VC buffer management to improve fairness, and even minimum ftp.netlab.ohio-state.edu/pub/jain/papers/kaband.ps

[uDatabase: A Toolkit for Constructing Memory Mapped Databases - Buhr, Goel, Wai \(1992\)](#) (Correct) (2 citations)

performance advantage. Reduced Need for Explicit Buffer Management A sophisticated buffer manager is applications such as CAD/CAM systems, text management and GIS [vO90]We argue that the performance Query Results Leaf Requests Shared Buffer Queue Search Index Records Index diskN disk3 disk2 plg.uwaterloo.ca/pub/uDatabase/POS5.ps.gz

[New Faster Kernighan-Lin-Type Graph-Partitioning Algorithms - Dutt \(1993\)](#) (Correct) (3 citations)

in the ordered set S S can be implemented as two queues S 1 and S 2 ,into which the nodes u i and v j of the Kernighan-Lin (K-L) algorithm to swap pairs of nodes to improve an existing partition of a to search more than a certain subset of d 2 node pairs to find the node pair with the maximum swap gain. www.eecs.uic.edu/~dutt/papers/iccad93.ps

[Canonical Conditional Rewrite Systems Containing Extra .. - Avenhaus.. \(1993\)](#) (Correct) (1 citation)

is decidable and terminating. We develop a critical pair criterion to prove confluence if R is strongly deterministic and all proper critical pairs are joinable. Note that no paramodulation pairs pairs are joinable. Note that no paramodulation pairs (overlapping into the conditions) and no www.agis.uni-sb.de/publications/deduktion/seki/SR-93/Avenhaus.SR-93-03.english.ps.Z

[Generic Properties of Combinatory Maps - Neutral.. - Reidys, Stadler.. \(1995\)](#) (Correct) (6 citations)

as lists of Watson-Crick (AU and GC) and GU base pairs. Base pairing and base pair stacking constitute Watson-Crick (AU and GC) and GU base pairs. Base pairing and base pair stacking constitute the major and GC) and GU base pairs. Base pairing and base pair stacking constitute the major contributions to the www.tbi.univie.ac.at/papers/Abstracts/95-07-04.ps.gz

[A Method for Analog Circuits Visualization - Arsintescu](#) (Correct)

an algorithm capable of finding symmetrical pairs in a general circuit structure without user one of them connected as a diode. ffl symmetrical pair: a pair of identical MOSTs connected with the same them connected as a diode. ffl symmetrical pair: a pair of identical MOSTs connected with the same donau.et.tudelft.nl/pub/bogdan/ICCD96-paper.ps.gz

[Confluence of Terminating Conditional Rewrite Systems Revisited - Gramlich, Wirth \(1996\)](#) (Correct)

to joinability of all (conditional) critical **pairs**. In other words, this means that variable are terminating CTRs which have joinable critical **pairs** but are not (locally) conAEuent. That means conAEuent. That means joinability of all critical **pairs** does not suOEce any more for inferring conAEuence [ftp.loria.fr/pub/loria/protheo/COMMUNICATIONS_1996/Gramlich-rta96b.ps.gz](http://loria.fr/pub/loria/protheo/COMMUNICATIONS_1996/Gramlich-rta96b.ps.gz)

Tooling the Lexicon Acquisition Process for Large-Scale KBMT - Leavitt (1994) (Correct) (1 citation)
is critical, as a set of source-target translation **pairs** is not a sufficient lexicon for an interlingua we developed a tool to align on-line document **pairs** and to facilitate extraction of terminology from them. A corpus of source/target document **pairs** is collected and automatically aligned using www.lti.cs.cmu.edu/Research/Kant/PostScript/take3.ps

Fine-granularity Locking and Client-Based Logging.. - Panagos, Biliris.. (1996) (Correct) (3 citations)
to the page server approach described in [6]The **buffer** managers of the clients and the server follow the In this paper, we describe how local transaction **management** is carried out in a data shipping client-server all shared locks held by the crashed client and **queues** any callback requests until the client recovers. www.research.att.com/~biliris/publications/papers/96_edbt.ps

REDUCE: a prototypical cooperative editing system - Chengzheng Sun (1997) (Correct)
as the local timers for flushing the input string **buffer** and for multicasting local status messages to of the following major components: 1) A Session Management Handler (SMH)which is a thread inside the SS www.cit.gu.edu.au/~scz/papers/hci97.ps.Z

Further Results in Affinity-Based Scheduling of Parallel.. - Salehi, Kurose, Towsley (1995) (Correct) (1 citation)
Thread Pool Packet **Queue** Available Processor Pool **Buffer** Pool P P 1 2 T T T 3 4 5 (a) b) c) Ip Udp Fddi networking involves not only the concurrent **management** of protocol threads and available processors, lower packet processing times and improved packet **queueing** behavior. Finally, we show that the benefit gaia.cs.umass.edu/pub/salehi/ca-tr2.ps.gz

Real-Time Computing with Lock-Free Shared Objects - Anderson, Ramamurthy, Jeffay (1997) (Correct) (11 citations)
mechanism based on waitfree read/write **buffers**. In their approach, all **buffer management** is systems D.4.1 [Operating Systems]Process **Management** -concurrency, either the "next" pointer of the last item in the **queue** or a head pointer, depending on whether the **queue** archi.snu.ac.kr/yhbae/paper/lockfree/Anderson_..._TACS-97.ps.gz

A Proposal For A User-Level, Message Passing Interface In.. - Dongarra, Hempel, al. (1993) (Correct) (25 citations)
: 6 3.1.4 **Buffering** of messages by the system : www.epm.ornl.gov/~walker/mpi/papers/mpi1.ps.Z

An ACL2 Proof of Write Invalidate Cache Coherence - Moore (1998) (Correct) (3 citations)
out-of-order instruction issue with a reorder **buffer**, speculative execution and exceptions. Proofs are Each element in an association list (or alist) is a **pair** consisting of a key and a datum. The key is said to values. A cache is an alist binding addresses to **pairs** of the form (value f lag)Such **pairs** are called www.cs.utexas.edu/users/moore/publications/wicache.ps.Z

A comparison of Protection Lookaside Buffers and the PA-RISC.. - Wilkes, Sears (1992) (Correct) (5 citations)
Report A comparison of Protection Lookaside **Buffers** and the PA-RISC protection architecture John encode the access types allowed, and a further two **pairs** of two bits to identify the privilege levels. server it is accessing. Such communication can be **pairwise** or N-way, depending on the intent. In such www.hpl.hp.com/research/itc/csl/ssp/papers/HPL-92-55.ps.Z

Decomposition methods for differentiable optimization problems.. - Patriksson (1997) (Correct) (1 citation)
that should be introduced to reach some traffic **management** goal without imposing a centralized traffic of the overhead caused by the handling of the task **queue** [73]We want to enforce the parallel CA or spatial price equilibrium problem [48]or **pairs** of origins and destinations in a transportation www.math.liu.se/~mipat/.LATEX/COAP.ps.Z

Information Resource Dictionary System Standards And Support For.. - Byrne (Correct)
IRDS standard framework uses regular Database Management System (DBMS) facilities as its base and also may be stored in the level underneath. The 'level **pairs**' referred to in the standard are the **pairs** that

'level **pairs**' referred to in the standard are the **pairs** that make up this type and instance **pair**. The www.uni-koblenz.de/fb4/publikationen/gelbereihe/RR-14-97/byrne.ps.gz

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No documents match Boolean query. Trying non-Boolean relevance query.

500 documents found. Order: relevance to query.

[Measuring the Capacity of a Web Server - Banga, Druschel \(1997\)](#) (Correct) (62 citations)

Router Web Clients Web Server Figure 3: Testbed **Architecture** In this section, we describe the design of a and places it in the listen socket's SYN-RCVD **queue**. Later, when the client responds with an ACK mechanism. 4.2 S-Clients A S-Client consists of a **pair** of processes connected by a Unix domain
www.cs.rice.edu/~gaurav/papers/web-paper.ps

[Priority Ethernet: Multimedia Support on Local Area Networks - Adelstein, Singhal \(1994\)](#) (Correct) (1 citation)

used interchangeably. Each node has two priority **queues**, called priority and pending. Each element in
dri.cornell.edu/pub/frank/papers/ismm94.ps.gz

[Efficient Low-Contention Parallel Algorithms - Gibbons \(1994\)](#) (Correct) (16 citations)

Proc. 7th ACM Symp. on Parallel Algorithms and **Architectures**, pages 84-94, July 1995. BH85] A. Borodin
78712 vlr@cs.utexas.edu May 21, 1996 Abstract The **queue-read**, **queue-write** (qrqw) parallel random access
p n keys, 2) sort the sample by comparing all **pairs** of keys, 3) each item determines by binary
www.bell-labs.com/user/matias/papers/qrqw2.ps

[Modeling Telecommunication Systems with Self-Similar Data Traffic - Fiorini \(1998\)](#) (Correct) (5 citations)

performance characteristics of **queues** (e.g. **buffer** overflow probability) 19]3) Conceptual
Impact of Autocorrelation on Queuing Systems" **Management Science**, 39, 332-339, 1993. 24] B. B.
. 6 2.2 The Linear Algebraic Approach to **Queueing** Theory .8 2.2.1 Representation of
www.engr.uconn.edu/~lester/papers/pierre.ps.gz

[Parallel Data Structures for Symbolic Computation - Yelick, Chakrabarti.. \(1995\)](#) (Correct) (4 citations)

runtime system provides support for thread **management**, as well as a global address space spanning the
solved using a scheduling data structure, a stack, **queue**, or priority **queue** in sequential programs. The
addition of new rewrite rules, called critical **pairs**, and the simplification of existing rules. New
www.cs.berkeley.edu/projects/multipol/papers/psls95.ps

[The Passport Control Problem or How to Keep an Unstable.. - Itai, Rodeh, Shachnai \(Correct\)](#)

while others are not. As the **queues** build up, **management** assigns additional officers to the unmanned
airport, department stores, and more) parallel **queues** are formed in front of control stations.
each customer in the system can be represented by a **pair** (i j) where $0 \leq i \leq \Gamma - 1$ is the number of
www.cs.technion.ac.il/~itai/publications/FUN.ps

[Integration of Reactive Navigation with a Flexible Parallel.. - Thomas Collins \(1993\)](#) (Correct) (2 citations)

Navigation with a Flexible Parallel Hardware **Architecture** Thomas R. Collins, Ronald C. Arkin, Andrew M.
ftp.cc.gatech.edu/pub/people/arkin/web-papers/integration.ps.Z

[Behavior Based Architecture with Distributed Selection - Correia, Steiger-Garção \(1993\)](#) (Correct)

Behavior Based **Architecture** with Distributed Selection Lus Correia *and
ssdi.di.fct.unl.pt/~lc/papers/trento.ps.gz

[The Next Frontier: Interactive and Closed Loop.. - Reed, Elford.. \(1996\)](#) (Correct) (22 citations)

to or from storage devices and application **buffers**. Conversely, small requests are better served by
mechanisms that select and configure resource **management** algorithms automatically, based on observed
file cache hit ratios, input/output server timings, **queue** lengths and delays, and prefetch initiation
www.cs.wm.edu/~esmirni/docs/icppw96.ps.gz

[CORBA as the Core of the TINA-DPE: A View from the Security.. - Staamann, Wilhelm \(1997\)](#) (Correct)

is concerned with security problems in the TINA **architecture**. It presents a structuring of this complex
the protection of information exchanged for the **management** of systems and services and access control to

lsewww.epfl.ch/Documents/postscript/SW97b.ps

JaDE: Access Control in a Java-Based Object Database - Jones Winslett (1995) (Correct) (1 citation)
will warrant reconsideration of current ODBMS **architectures**, since the typical **architecture** today performance. 1 Goals Today's object database **management** systems (ODBMSes) support client/server
drl.cs.uiuc.edu/security/./pubs/oowkshop.ps

Development and Control of Distributed Multimedia Applications - Tobias Helbig (1994) (Correct) (1 citation)
The CINEMA (Configurable INtEgrated Multimedia **Architecture**) system is such a middleware layer which System and Network Services (e.g. Scheduling, **Buffer Mngt.Data Communication**) ApplicationOriented reservation, synchronization and configuration **management**. By that, the gap between system services of 129.69.211.2/ipvr/vs/alt/helbig/./Publications/1994-helbig-01.ps.Z

Incremental Process Support for Code Reengineering: An ... - Kaiser, Heineman.. (1996) (Correct)
or user interface. In some cases, a new **architecture** can be developed, for example to convert a system to new technology, e.g.a new database **management** system or user interface toolkit. Another is to Area. Treaties between Oz sites are set up on a **pairwise** basis that is neither symmetric nor
ftp.cs.columbia.edu/reports/reports-1996/cucs-007-96.ps.gz

A Spoken Dialogue System for German Intercity.. - Eckert, Kuhn.. (1993) (Correct) (7 citations)
speed) we found encouraging results. 2 **Architecture** The hierarchical structure of our Sundial the generation of system utterances and **management** of a coherent and natural dialogue. While the
www5.informatik.uni-erlangen.de/TeX/Literatur/ps-dir/1993/Eckert93:ASD.ps.gz

DARWIN: On the Incremental Migration of Legacy Information.. - Brodie, Stonebraker (1993) (Correct) (12 citations)
(i.e.infrastructure) such as a client-server **architecture**, DBMSs and CASE. We illustrate the methods of the IS and the associated planning and **management** to achieve an incremental migration that is
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A Performance Comparison of TCP/IP and MPI on FDDI, Fast.. - Nog, Kotz (1996) (Correct)
that contribute to the final results: machine **architecture**, network protocol software, network card in set both the sending and receiving side kernel **buffers** at 64K bytes (maximum allowed on the RS6000s, the set of all participating hosts. ffl Bandwidth/Pair of processes: the average bandwidth each
casaturn.kaist.ac.kr/~sikang/load/papers/NK95.ps.gz

Characterization of Integrated Process and Product Management - Joeris (Correct)
Of Integrated Process And Product **Management** Gregor Joeris Intelligent Systems Department,
www.informatik.uni-bremen.de/~joeris/pub/wgaip97.ps.gz

Scientific Workflow Management: WASA Architecture and.. - Weske, Vossen, Medeiros (1996) (Correct)
Scientific Workflow **Management: WASA Architecture** and Applications Mathias Weske, Gottfried Scientific Workflow **Management: WASA Architecture** and Applications Mathias
[4, 6]Today, short sequences of DNA (500 base **pairs**) can be generated semiautomatically, using
wwwmath.uni-muenster.de/math/inst/info/u/dbis/Weske/Common/./Papers/fb03-96.ps.gz

Incorporating Syntactic Constraints in Recognizing.. - Srihari, Baltus (1993) (Correct) (8 citations)
images for the next stage 1 The word **buffer** is fed into the wholistic filter whose purpose is between the syntactic categories represented by **pairs** (or n-tuples) of entities in the candidate probabilities P (W ord j Tag) for each word/tag **pair**. First order transition statistics were collected
www.cedar.buffalo.edu/~rohini/Postscript/ijcai93.ps.Z

A comparison of ABR and UBR to support TCP traffic - Manthorpe, Le Boudec (Correct)
protocol stack. Figure 2 shows the **architecture** of a **pair** of model workstations. In STCP, We test the hypothesis that UBR with adequate **buffering** in the ATM switches results in better overall
Apr. 1991. 5] ATM Forum, ATM Forum Traffic **Management** Specification version 4.0, revision 10,
lrcwww.epfl.ch/PS_files/abrubr.ps.gz

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[Dealing with Higher Dimensions: The Well-Separated Pair.. - Paul B. Callahan \(1995\) \(Correct\) \(13 citations\)](#)
are implemented quite efficiently on existing **architectures**. There are two reasons that one should always
Dealing with Higher Dimensions: The Well-Separated Pair Decomposition and Its Applications Paul B.
most naturally in terms of the set of all distinct **pairs** of points, along with the Euclidean distance
<ftp.cs.jhu.edu/pub/callahan/dissertation.ps.Z>

[Persistent Route Oscillations in Inter-Domain Routing - Varadhan, Govindan, Estrin \(1996\) \(Correct\) \(52 citations\)](#)

to particular destinations. The Unified routing **architecture** [9] uses a combination of **PAIRS** based
path-attribute based, independent, route selection (**PAIRS**)¹ to describe this type of distributed
route computation. A simplified description of **PAIRS** follows. Each domain receives one or more routes
ftp.isi.edu/eddy/ra-papers/bgp_osc.ps

[Dynamic Bandwidth Allocation for ATM Switches - Hsu, Walrand \(1995\) \(Correct\) \(22 citations\)](#)

1.1 we introduce the output-**buffered** switch **architecture** and show how the contended resources, i.e.
sharing of network resources (e.g. switch **buffers** and transmission bandwidth) among users.
of the output line **buffer**. upper bound on the **queueing** delay can be guaranteed. This provides a way
www.cl.cam.ac.uk/Research/SRG/measure/papers/entropy/hsu.dynamic-bw-alloc.ps.gz

[Modeling Caching Effect in Continuous Media Server - Kang, Yeom \(1999\) \(Correct\)](#)

6, 3] Two important resources -disk bandwidth and **buffer** capacity -are considered when deciding the
[1] Asit Dan and Dinkar Sitaram. **Buffer management** policy for an on-demand video server. Technical
dcslab.snu.ac.kr/~yeom/paper/mascots99.ps

[Variation Fluctuation Smoothing for ATM Circuit Emulation.. - McDonald, Liao, Giroux \(Correct\)](#)

which usually imposes a constraint on the play-out **buffer** size at the receiver end. Therefore the
[1] ATM Forum Technical Committee, Traffic Management Specification, Version 4.0"ATM Forum
through the switch with a minimum deterministic **queueing** delay. The cells from a common VC would
www.uottawa.ca/~dmcdonal/papers/ces.ps

[The Video Z-buffer: A Concept for Facilitating Monoscopic.. - Sriram Sethuraman \(1996\) \(Correct\)](#)

The Video **Z-buffer**: A Concept for Facilitating Monoscopic Image
sequence coding context in [5]A stereoscopic **pair** of frames were segmented based on disparity and
off between a continuum of stereoscopic image **pairs** with both less than the extreme stereoscopic
almond.srv.cs.cmu.edu/afs/cs/project/sensor-9/ftp/papers/sriram_hdtv96.ps

[Efficient Redo Processing in Main Memory Databases - Lin, Dunham, Li \(1996\) \(Correct\)](#)

time update X in main memory Redo Rule: flush log **buffer** to nonvolatile log checkpoint X (clean, no
and Development Committee of the Community Management Staff. Efficient Redo Processing in Main
the log. A begin-chkpt-record and end-chkpt-record **pair** defines a complete checkpoint interval. Since all
www.seas.smu.edu/~jun/Papers/RLAW/tr.ps.gz

[Nonlinear Predictive Rate Control For Constant Bit Rate Mpeg.. - Yoo-Sok Saw \(Correct\)](#)

video rate control which employs a transmission **buffer** for constant bit rate video transmission. A
www.ee.ed.ac.uk/~sasg/Papers/97_papers/ICASSP97_ys.ps

[Proof of Correctness of a Processor with Reorder.. - Hosabettu, Srivas.. \(1999\) \(Correct\) \(8 citations\)](#)

L. Hennessy and David A. Patterson. Computer **Architecture**: A Quantitative Approach. Morgan Kaufmann, San
Proof of Correctness of a Processor with Reorder **Buffer** using the Completion Functions Approach Ravi
as an unbounded **buffer** as opposed to a circular **queue**. 1 At the specification level, the state is
www.cs.utah.edu/~hosabettu/publications/cav99.ps.gz

Can User-Level Protocols Take Advantage of Multi-CPU NICs? - Piyush Shivam Dept (2002) (Correct)
(2 citations)

VIA interface is already included in the latest **InfiniBand Architecture** (IBA) 3] as the Verbs layer. 1 standard in terms of the Virtual Interface **Architecture** (VIA) 13]An extension to the VIA interface send the frame. The transfer is made in the send **buffer** which is updated after each transfer. This set of <ftp.cis.ohio-state.edu/pub/communication/papers/paremp.pdf>

Using PCI-Bus Systems in Real-Time Environments - Schönberg (2002) (Correct)

. 11 2.1.4. **InfiniBand Architecture** .

of Computer Science Institute for System **Architecture** by Dipl.Inform. Sebastian Schonberg born along with each transaction to make host bu#er **management** more e#cient [8]A smooth migration to the new os.inf.tu-dresden.de/papers_ps/schoenberg-phd.ps

Architectural Software Support for Processing Clusters - Gutleber, Cano, Cittolin, ... (2000) (Correct)

idea behind upcoming I/O approaches, such as the **Infiniband architecture** [22]data are transferred from COTS and custom hardware components. Software **architecture** involves the description of elements from that are outlined in figure 2. The inbound **queue buffers** messages that originate from the host and the <gutleber.home.cern.ch/gutleber/cluster2k.pdf>

High Performance User Level Sockets over Gigabit Ethernet - Piyush (Correct)

10]It has also led to the development of the **InfiniBand Architecture** (IBA) 1]These developments are Sockets, User-level protocol, Interprocessor **Architecture** 1 Introduction Networks of Workstations so on. EMP is a zero-copy protocol as there is no **buffering** of the message at either the NIC or the host, ftp.cis.ohio-state.edu/pub/communication/papers/cluster02_soemp.pdf

Linux/SimOS - A Simulation Environment for Evaluating.. - Won, Lee, Yu (2002) (Correct) (1 citation)

as Virtual Interface **Architecture** (VIA) 10] and **InfiniBand Architecture** (IBA) 11]As these new protocols protocols, such as Virtual Interface **Architecture** (VIA) 10] and **InfiniBand Architecture** (IBA) DMA operations using descriptors pointing to DMA **buffers**. Typically, the Linux NIC driver allocates DMA <www.ece.orst.edu/%7Ebeni/Publications/icpp2002.pdf>

ShiDan95] Kang G. Shin and Stuart W. Daniel. "Analysis.. - On Computer Architecture (Correct)

no. 6, pp. 1070-1081, June 1992. Inf01] The **Infiniband Architecture** ,www.infinibandta.org, International Symposium on Computer **Architecture**, Santa Margherita Ligure, Italy, pp. 211-219, L. Frazier, Dynamically-Allocated Multi-**Queue Buffers** for VLSI Communication Switches"IEEE www.ee.princeton.edu/~peh/publications/thesis/references.pdf

Scalable Networking for Next-Generation - Computing Platforms Yoshio (Correct)

adapter using the **Queue Pair** model of the **Infiniband TM Architecture** [4]The TCP Servers project applications. The first component is the ETA **architecture** developed at Intel Labs, where one or more the socket connection interfaces, and support the **buffering** semantics of TCP streams. Each DTI consists of www.hpl.hp.com/personal/Yoshio_Turner/san3.pdf

VIBe: A Micro-benchmark Suite for Evaluating.. - Banikazemi, Liu.. (2000) (Correct)

VIA features are also included in the emerging **InfiniBand Architecture** IBA) 2]The VI **architecture** Suite for Evaluating Virtual Interface **Architecture** (VIA) Implementations M. Banikazemi J. Liu S. results. Similarly, a latency test where **buffers** are reused will have significant difference in <ftp.cis.ohio-state.edu/pub/communication/papers/ipdps01-VIBe.pdf>

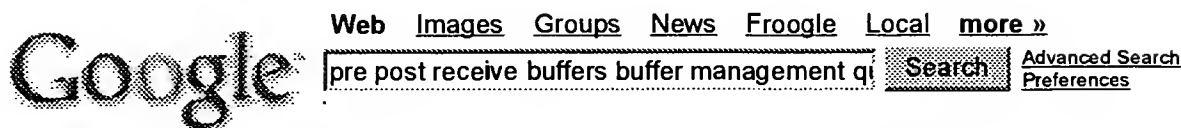
CSP: A Novel System Architecture for Scalable.. - Shah, Minturn.. (2001) (Correct) (8 citations)

Virtual Interface (VI) **Architecture** and emerging **Infiniband Architecture** [9]were developed to eliminate and Systems (USITS `01) CSP: A Novel System **Architecture** for Scalable Internet and Communication the complexity of session **management** and TCP data **buffering** to the web-switch. We have observed an discolab.rutgers.edu/classes/labmeeting/papers/csp-usits01.pdf

An Evolutionary Approach to System-Level Synthesis - Teich, Blicke, Thiele (1997) (Correct) (19 citations)

onto a heterogeneous hardware/software **architecture**. This problem requires (1) the selection of www.blicke.handshake.de/publications/codes.ps

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Software Architecture Specification (SAS)

... posting **receive** work request, when they usually **pre-post** all the **receive** ...
queue pairs and completion queues and registered the memory **buffers** ...

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... when they usually **pre-** **post** all the **receive** requests. ... vendors may allocate **buffers** for Completion Queue in user mode and program hardware ...

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... the **queue pairs** at the two parties before they can communi- ... can **pre-post** a number of **receiver buffers** for each con-. nection. ...

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... the message is put into an unexpected **queue** and later copied to the **receive buffer**. ... The receiver will **post buffers** for this particular. size only. ...

nowlab.cis.ohio-state.edu/publications/ tech-reports/2002/liuj-tr.pdf - [Similar pages](#)

Patent 5299313: Network interface with host independent buffer ...

... in the **pre-specified** block of addresses to the transfer descriptor **buffer** in ... or **buffer management** functions for the transmit and **receive** operations. ...

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... **buffer/descriptor management** as well as memory registra- ... that **buffers/descriptors** be **pre-posted** on the **receive**. work **queue** to **receive** messages. ...

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... channel adapters (HCA/TCA) provide **queue pairs** (QP, one for read and one ... wants to send data, and the sink didn't **post receive buffers** (in case of ...

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... module must manage a **queue** of **receive buffers** that can be ... BMI user posts **receive buffer**. 2. **Post** the **receive buffer** to the rendezvous VI ...

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[PPT] MS Group

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... Link layer – receiver-controlled to avoid buffer overflow ... Arbitrary number of queue pairs, HCA ports, and virtual lanes ...

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[PDF] [PVFS over InfiniBand: Design and Performance Evaluation](#)

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... and response message pairs. 5.2. Server RDMA Buffer Management. Server RDMA buffers are used to receive data from clients and to read data from files. ...

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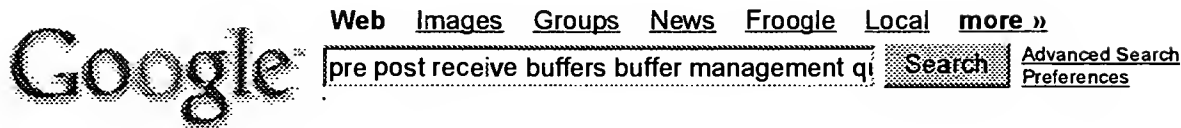


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From: jsutton@iWarp.intel.com (Jim Sutton) Subject: iWarp ...
 ... **Pre-** and **post**-increment addressing. * Small-constant or variable address ...
 data directly from/to a channel **buffer's** data **queue** by reading/writing ...
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 ... as work **queue**). For data transmission, the sender and the **receiver post** ...
 block, NetPIPE-VIA **pre**-allocates and **pre**-registers a pool of memory **buffers**. ...
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 ... two queues: a send **queue** and a **receive queue**. The send ... and response message
pairs. 3.3.2 Server RDMA **Buffer Management** ...
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 ... ways created in **pairs**. The QP - **Queue Pair** is composed. of one WQ for send
 operations and ... the **receive buffer** has to be registered. In case of RDMA- ...
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 ... The size of the **receive buffers** has to be larger or equal to the size of the
 ... called WQ - Work Queues, are always created in **pairs**. The QP - **Queue** ...
www.tu-chemnitz.de/informatik/service/if-berichte/pdf/CSR-03-05.pdf - [Similar pages](#)

[PDF] 1996 International Conference on Parallel Processing 1
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 ... **buffer management** to implement zero-copy protocols. to achieve very low-latency
 message ... **buffer**, the **receiver** puts the **buffer ID** into the **queue**; ...
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 ... **Pre**-diagnostic Test; Instruction Management; **Post**-diagnostic Test ...
 algorithm where the entries in the ready **queue** are pointers to the PCBs. ...
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 ... **post**. a descriptor onto the message **queue** of a VI without block- ... from the
pre-registered **receiving buffer** into the user. **buffer**. ...
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... NIC interaction is required on each I/O to **pre-post**. application **receive buffers**.
 ... access to databases of key/data **pairs**. It is linked into the ...
www.eecs.harvard.edu/~magoutis/fast-final.pdf - [Similar pages](#)

[PS] To appear in The Proceedings of The International Conference on ...
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 ... the **receiving** process upon packet arrival, the **receive buffer** must be ...
 (The number of entries in a **queue** can never exceed the number of **buffers** the ...
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